UCD-1 QLV

USB connected
Quad LVDS capture device

USB Connected Frame Grabber

UCD series is a family of compact sized capture devices utilizing USB interface. They provide a flexible and robust way of image capture in automated testing of display related ASICs and display electronics. The dedicated hardware and the easy to apply SW library provide an optimal platform for creating sensitive but short cycle time routines for test systems.

UCD series frame grabbers are designed for test applications. They provide the captured image bit-to-bit in the way it was received without any compression or data loss. The software routines provide full control of the interface and options for e.g. color depth selection, pixel mapping and channel allocation configuration. The dedicated software API is tailored for the purpose, no DirectShow overhead included.
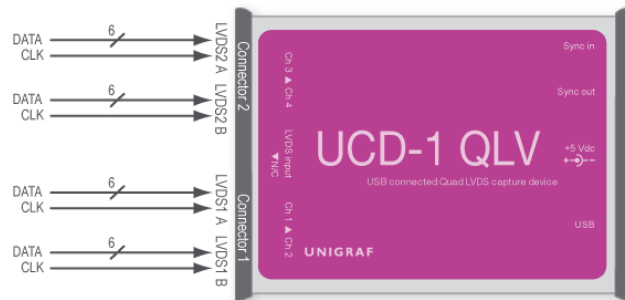
Quad LVDS

UCD-1 MLC is a frame grabber that can capture four (4) LVDS channels. It enables automated testing of LCD TV main boards with Quad LVDS output. The used link configuration, color depth and pixel mapping is defined with configuration data.

Highlights

- Compact, USB connected device
- Captures 4 parallel LVDS channels
- VESA and Jeida color modes with 18 to 36 bits per pixel.
- Easy to use SW SDK for Windows and Linux

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UCD-1 QLV

USB connected Quad LVDS capture device

Buffering Frame Grabber

UCD series capture devices provide a flexible way of retrieving the video output of the D.U.T. Their role is to replace part of the display electronics and provide the received image for the test software for evaluation.

Windows and Linux

The use of UCD devices is easy since they are compatible with any PC with an USB connection. Drivers and software are available for both Windows and Linux operating system. SDK with example application help the designer for a short design-in time.

UCD series currently includes capture devices for mini-LVDS, LVDS and V-by-One capturing. Additional interfaces will be introduced soon.

Flexible Configuration

UCD-1 QLV adapts to both VESA or Jeida pixel color mapping. Through configuration files the user can also define the used color depth.

Specifications

Image Data Input	4 parallel LVDS channels with 6 data lanes. Clock input for Each channel
Input Connector	2 x FI-RE51S-HF-R1500 (JAE Electronics)
Pixel Rate	100 MHz / channel maximum
Input Configuration	6, 8, 10 or 12 bits / color.
Pixel Mapping	VESA and Jeida
Capture Speed	Approximately 4 FHD frames / second
Computer Interface	USB 2.0
Sync In / Out	Synchronizing input and output for Master / Slave configuration
Operating System	Windows 8, 7 and XP, Linux
SW SDK	SW API with Example and preview applications
Power Input	+5 Vdc (AC/DC converter included)
Module Size	TBD
Weight	TBD

All specifications subject to change without notice.

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